

SIDLEY & AUSTIN PARTNERSHIP INCLUDING PROFESSIONAL CORPORATIONS

717 North Harwood DALLAS, TEXAS 75201 TELÈPHONE 214 981 3300 FACSIMILE 214 981 3400



FOUNDED 1866

WRITER'S DIRECT NUMBER (214) 981-3304

WRITER'S E-MAIL ADDRESS rchauza@sidley.com

February 15, 2000

BOX: PATENT APPLICATION

Assistant Commissioner

for Patents

Washington, DC 20231

Re:

4

m

Inventor: Dario B. Crosetto

Our File: 10652/502

EXPRESS MAIL NO __EL072274401US DATE OF DEPOSIT __February 16, 2000

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee' service under 37 CFR 1 10 on the date indicated above and is addressed to BOX. PATENT APPLICATION, Assistant Commissioner for Patents, Washington, DC 20231.

Dario B Crosetto

Name of Depositor

Signature

Date of Signature. February 16, 2000

Dear Sir:

REQUEST FOR FILING A PATENT APPLICATION

Enclosed for filing are the following papers relating to a Patent Application, entitled METHOD AND APPARATUS FOR EXTENDING PROCESSING TIME IN ONE PIPELINE STAGE:

Specification, Claims, and Abstract (74 pages);

Declaration and Power of Attorney;

Verified Statement (Declaration) Claiming Small Entity Status - Independent Inventor:

Informal Drawings (56 sheets): A check in the amount of \$345; and

This patent application is a § 111(a) patent application, claiming priority on U.S. Patent Application Serial No. 60/120,194, entitled IMPLEMENTATION OF FAST DATA PROCESSING WITH MIXED-SIGNAL AND PURELY DIGITAL 3D-FLOW PROCESSING BOARS, filed February 16, 1999, which is currently pending.

Please file this patent application, indicate the assigned serial number on the enclosed postcard and return same to me.

In the event the attached check is unacceptable, is insufficient, or the check is omitted, please charge the required fee to the Sidley & Austin Deposit Account No. 18-1260. If the amount is in excess, please credit Sidley & Austin Deposit Account No. 18-1260.

Respectfully submitted.

Registration No. 29,753

RNC/lr Enclosures